



Docket No.: ESEC-P220US

IFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Bollinger, Daniel
SERIAL NO.: 10/719,829
FILING DATE: November 20, 2003
TITLE: Method For Picking Semiconductor Chips From A Foil
EXAMINER: GREENHUT, CHARLES N
ART UNIT: 3652

**COMMISSIONER FOR PATENTS
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STATUS REQUEST LETTER

Please advise the status of the above-identified patent application.

Respectfully submitted,
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Dated: March 7, 2006

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